



# VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY : BURLA

(Formerly University College of Engg., Burla- Established by Govt. of Odisha in 1956 & Upgraded in 2009 to A State Govt. University Covered under Section 2(f) & 12(B) of UGC Act.)

P.O: Engineering College, Burla (Siddhi Vihar), Dist: Sambalpur  
Odisha - 768018, INDIA

No. VSSUT/ 888 /

2017

Dated: 18-10-2017

## NOTICE

The **Workshop on High Performance Computing** will be organized by **C-DAC, Pune** from **25-28 October 2017** at **Sir Visveswaraya Auditorium, VSSUT Burla**. The detailed programme is attached herewith for information of all concerned. All the interested faculty members and students are required to register by submitting a registration form in prescribed format to **PIC, Supercomputing Facility, Prof. A.K. Rath**, Professor of Computer Science and Engineering through their respective HODs **on or before 23rd October 2017 (Monday)**.

*N.B: The number of seats for the workshop is limited to 150 only.*

By order of Vice-Chancellor

  
REGISTRAR

Memo No. VSSUT/OOR/ 889 (30)

Dated: 18-10-2017

Copy to :

1. Prof. A.K.Rath, Professor of CSE & PIC, Supercomputing Facility for information and necessary action
2. All HODs for information and necessary action.
3. All Deans/COF for information necessary action.
4. Dean, Faculty & Planning with a request to hoist this notice in University website.
5. PIC, Electrical Maintenance/Civil Maintenance/CIF/CCF for information and necessary action.
6. ME/Campus Supervisor for information and necessary action
7. PA to VC for kind information of Hon'ble Vice-Chancellor

  
REGISTRAR

## WORKSHOP ON HIGH PERFORMANCE COMPUTING

Organized by

**Centre for Development of Advanced Computing (C-DAC)**

**Ministry of Electronics and IT, Government of India**

**At**

**Veer Surendra Sai University of Technology, Burla**

**Government of Odisha**

Dates: October 25 – 28, 2017

Venue: VSSUT, Burla

**Day 1 (Wednesday): October 25, 2017**

Time (Hrs)	Sessions
10:00 – 11:00	<b>High Performance Computing, IoT and Big Data</b> <i>Keynote Speaker: Prof. Lalit Mohan Patnaik, IISc, Bangalore</i>
Break	
11:15 – 12:15	<b>High Performance Computing, IoT and Big Data contd..</b> <i>Keynote Speaker: Prof. Lalit Mohan Patnaik, IISc, Bangalore</i>
12:15 – 13:00	<b>Insight to Parallel Computing</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
Lunch	
14:00 – 15:30	<b>Intel architecture, software tools and technologies</b> <i>Speaker: Jyotsna Khemka, Intel Corporation, Bangalore</i>
Break	
15:45 – 17:00	<b>Intel architecture, software tools and technologies contd..</b> <i>Speaker: Jyotsna Khemka, Intel Corporation, Bangalore</i>

**Day 2 (Thursday): October 26, 2017**

<b>Time (Hrs)</b>	<b>Sessions</b>
10:00 – 11:00	<b>Advance Parallel Computer Architecture</b> <i>Speaker: Narendra Singh, C-DAC, Pune</i>
<b>Break</b>	
11:15 – 11:45	<b>Insight to PARAM Shavak HPC and Deep Learning System Achitecture and Systems</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
11:45 – 12:15	<b>Programming Aspects with Linux OS</b> <i>Speaker: Pallav Jain, C-DAC, Pune</i>
12:15 – 13:00	<b>Open Multi-Processing (OpenMP)</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
<b>Lunch</b>	
14:00 – 15:30	<b>Open Multi-Processing (OpenMP) Cont...</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
<b>Break</b>	
15:45 – 16:30	<b>Demonstration of Applications using OpenMP</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
16:30 – 17:00	<b>Compute Unified Device Architecture (CUDA) Overview</b> <i>Speaker: Samrit Maity, C-DAC, Pune</i>

**Day 3 (Friday): October 27, 2017**

<b>Time (Hrs)</b>	<b>Sessions</b>
10:00 – 11:00	<b>Usage of HPC Application and Linux Hands on</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
<b>Break</b>	
11:15 – 12:15	<b>Advanced CUDA Programming</b> <i>Speaker: Samrit Maity, C-DAC, Pune</i>
12:15 – 13:00	<b>Profiling on CUDA</b> <i>Speaker: Samrit Maity, C-DAC, Pune</i>
<b>Lunch</b>	
14:00 – 15:00	<b>Deep Learning Technologies demonstration</b> <i>Speaker: Samrit Maity, C-DAC, Pune</i>
15:00 – 15:30	<b>Programming Aspects using Deep Learning</b> <i>Speaker: Samrit Maity, C-DAC, Pune</i>
<b>Break</b>	
15:45 – 17:00	<b>Programming Aspects using Deep Learning contd..</b> <i>Speaker: Samrit Maity, C-DAC, Pune</i>

**Day 4 (Saturday): October 28, 2017**

<b>Time (Hrs)</b>	<b>Sessions</b>
10:00 – 11:00	<b>Message Passing Interface (MPI) Programming</b> <i>Speaker: Narendra Singh, C-DAC, Pune</i>
<b>Break</b>	
11:15 – 12:15	<b>Message Passing Interface Programming Contd..</b> <i>Speaker: Narendra Singh, C-DAC, Pune</i>
12:15 – 13:00	<b>Uniprocessor programming</b> <i>Speaker: Shweta Das, C-DAC, Pune</i>
<b>Lunch</b>	
14:00 – 15:00	<b>Introduction to Shell Scripting</b> <i>Speaker: Pallav Jain, C-DAC, Pune</i>
15:00 – 15:30	<b>Demonstration and Practical Hands – On Session</b> <i>Speaker: Pallav Jain, C-DAC, Pune</i>
<b>Break</b>	
15:45 – 17:00	<b>Ganglia Monitoring</b> <i>Speaker: Pallav Jain, C-DAC, Pune</i>

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**(October 25 – 28, 2017)**



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**Centre for Development of Advanced Computing (C-DAC)**  
**Ministry of Electronics and IT, Government of India**

**&**

**Veer Surendra Sai University of Technology, Burla Government**  
**of Odisha**

**Application / Registration Form**

**NAME (in block letters) :**

**Highest Qualification :**

**Department :**

**Designation :**

**Areas of Interest :**

**Correspondence Address :**

**Mobile Number :**

**E-mail :**

**Signature of the Applicant**