



No.VSSUT/SRIC/ 387/2026

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY  
PO- ENGINEERING COLLEGE BURLA, DIST-SAMBALPUR, ODISHA-  
768018  
www.vssut.ac.in

Date: 11/05/2026

**WALK-IN (OFFLINE) INTERVIEW**  
**JUNIOR RESEARCH FELLOW & PROJECT ASSOCIATE POSITIONS UNDER ANRF-PAIR**  
**SCHEME**

Title of Project: Consortium of Technologies for Sustainable Agriculture, Health, Energy And Environment  
Date & Time of Interview: 20.06.2026, 9.00AM  
Venue: Golden Jubilee Seminar Hall

The Veer Surendra Sai University of Technology, Burla invites candidates to attend a Walk-in Interview for the positions of Junior Research Fellow (JRF) and Project Associate (PA) under the ANRF-PAIR scheme. Interested candidates are advised to send a single PDF file containing the completed application form and supporting documents (Mark Sheets, Certificates, Research Publications, if any) via e-mail, mentioning the "Advertisement No." in the subject line, to the following email address: [anrfpair@vssut.ac.in](mailto:anrfpair@vssut.ac.in)

Closing date of online application: 18.06.2026

Candidate are required to bring the hard copy of duly filled and signed application form (available on the University website: [www.vssut.ac.in](http://www.vssut.ac.in)) along with all supporting documents, including mark sheets, certificates, and research publications (if any) at the time of the Walk-in Interview. No TA/DA will be paid for attending the interview.

Positions	Department wise No of Positions	Duration	Monthly Fellowship/Stipend
JUNIOR RESEARCH FELLOW (JRF) / PROJECT ASSOCIATE (PA)	Chemical Engg - 01, Mechanical Engg. - 01 Electrical & Electronics Engg - 01 Civil Engineering - 01	Coterminous with the project	JRF: First 2 Yrs. - Rs. 37000 Beyond 2 Yrs - Rs. 42000 PA: Rs. 30000 pm till the completion of project No HRA will be provided
PROJECT ASSOCIATE (PA)	Electronics & Telecom. Engg - 05, Electrical Engg. - 01, Chemical Engg. - 01	Coterminous with the project	Rs. 30000 pm till the completion of project No HRA will be provided

*N.B. - All the JRF & PA those who will be selected have to enroll into Ph.D Programme at VSSUT.*

*Application form and detailed information regarding eligibility and educational qualification for JRF and PA, is available in University website [www.vssut.ac.in](http://www.vssut.ac.in). The authority reserves the right to accept/reject any or all the applications or the entire selection process without assigning any reason thereof.*

Memo No. VSSUT/SRIC/ 388<sup>(6)</sup>/2026

Copy to:

1. University Notice Board.
2. All Deans/HODs/COE for information & necessary action.
3. Director I & PR Department, Government of Odisha, Bhubaneswar, for the publication of PhD Admission Notice in all Odisha edition of, "The Samaj" (Odia) on 12/05/2026.
4. Dean, Faculty and Planning, with a request to display the advertisement in University website.
5. Registrar/Comptroller of Finance for information.
6. P.A to Vice-Chancellor for kind information of Hon'ble Vice-Chancellor.

Date: 11/05/2026

REGISTRAR

REGISTRAR



**VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA, ODISHA, INDIA**  
**APPLICATION FORM FOR WALK-IN INTERVIEW**  
**JUNIOR RESEARCH FELLOW & PROJECT ASSOCIATE POSITIONS UNDER ANRF-PAIR SCHEME**

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1. Advertisement No. \_\_\_\_\_
2. Project Code \_\_\_\_\_
3. Post Applied For \_\_\_\_\_
4. Department to which Applying for \_\_\_\_\_
5. Name of the candidate in full  
(Block Capital letters) \_\_\_\_\_
6. Father's Name \_\_\_\_\_
7. Broad area only \_\_\_\_\_
8. Name of Co-PI \_\_\_\_\_
9. Address for Communication \_\_\_\_\_

Telephone: \_\_\_\_\_ Mob: \_\_\_\_\_ Email: \_\_\_\_\_

10. Date of Birth: \_\_\_\_\_ 8. Sex : Male/Female 9. Marital status: Married /Single  
10. Whether GEN/SC/ST \_\_\_\_\_ 11. Nationality: \_\_\_\_\_ 12. Mother Tongue: \_\_\_\_\_  
13. Academic career: (Enclose attested copies of the certificates and mark sheets/grade cards showing the percentage of marks (CGPA) from H.S.C Examinations or equivalent)

Name of Exams	Institute/University	Year of passing	Branch/Subjects studied	Percentage of Marks/CGPA

**Attach Supporting Documents**

14. Whether Qualified Gate/Net (Yes/No) \_\_\_\_\_,  
If yes mention Gate/Net Subject: \_\_\_\_\_, Qualifying Year : \_\_\_\_\_, Score: \_\_\_\_\_

15. Experience if any

Organization	Position	Duration	Nature of job



	Project Code	Post	Broad area of research	Essential Qualification	Department/ Location of Interview	Co-PI
<b>Agriculture</b>						
	A1.3	Project Associate (PA)	IoT based AI Optimized Real-time Monitoring System with IVR service for Remote Aquafarm	<p><b>Essential qualifications:</b>  B.Tech in Electronics / Electronics and Telecommunication/Electronics &amp; Communication/ Electronics &amp; instrumentation / Electrical &amp; Electronics/ M.Tech in Electronics / Communication system /Electronics &amp; Communication/ Electronics &amp; instrumentation / Electrical &amp; Electronics RF engineering /RF and microwave OR M.Sc in Electronics/ Electronics and Telecommunication/Electronics &amp; Communication</p> <p><b>Desirable qualification:</b> B.Tech (GATE)/M.Sc (GATE/ Any national level scholarship) and with a CGPA of 7.0 or 65% marks. ME/M.Tech with at least 6.5 CGPA or 60% marks. Desirable soft skill: MATLAB, python, HFSS/ CST</p>	<b>ETC</b>	Diptimayee Konhar <a href="mailto:dkonhar_etc@vssut.ac.in">dkonhar_etc@vssut.ac.in</a>
	A1.5	Project Associate (PA)	SMART AGRI-NET: AIoT-Enabled Integrated Platform for Crop Health Monitoring, Precision Farming,	<b>Essential Qualification: B.E./B. Tech. in Electronics and Communication Engineering/Electronics and Telecom Engineering/Electrical/Electric</b>	<b>ETC</b>	Manasa Ranjan Jena <a href="mailto:mrjena_etc@vssut.ac.in">mrjena_etc@vssut.ac.in</a>

			and Sustainable Millet Cultivation	<p><b>al and Electronics Engineering/Electrical Engineering/Electronics and Instrumentation Engineering or similar with above 60% marks or 6.5 CGPA Students with higher qualification as given below M.E./M. Tech. in Electronics/Electrical/Instrumentation or similar with specialization in Communication Systems/Communication/VLSI/Microwave and Antenna or similar field with above 60% marks or 6.5 CGPA at both UG and PG levels have added advantage</b></p> <p><b>Desirable Skills: GATE score is desirable for B. Tech. students, Coding Skills in MATLAB/Python/ Verilog/ Xilinx Vivado</b></p>		
	A1.5	Project Associate (PA)	<p>SMART AGRI-NET: AIoT-Enabled Integrated Platform for Crop Health Monitoring, Precision Farming, and Sustainable Millet Cultivation</p>	<p><b>Essential Qualifications: BE/B Tech(CSE/EE/EEE/ECE/ETC/IT) with minimum of 6.5 CGPA in 10 point scale or minimum 60% mark</b></p> <p><b>OR</b></p> <p><b>M.Sc (Electronics/Computer Science/Data Science/IT) with with minimum of 6.5 CGPA in 10 point scale or minimum of 60% mark</b></p> <p><b><u>OR</u></b></p> <p><b>M Tech (CSE/ EE/ EEE/ ETC/ ECE/ IT) with with minimum of</b></p>	<b>ETC</b>	<p>Nilamani Bhoi  <a href="mailto:nbhoi_etc@vssut.ac.in">nbhoi_etc@vssut.ac.in</a></p>

				<p>6.5 CGPA in 10 point scale or minimum of 60% mark</p> <p>Desirable Qualification and Experience: GATE Score, Relevant industry experience</p>		
	H2A	Project Associate (PA)	Development of remote patient monitoring device with 5G secured communication.	<p>Essential Qualification: Minimum Qualification: B.E./B.Tech. in Electronics and Communication Engineering/ Electronics and Telecom Engineering/Electrical/Electrical and Electronics Engineering/Electrical Engineering/Electronics and Instrumentation Engineering or similar with above 60% marks or 6.5 CGPA.</p> <p>Students with higher qualification as given below M.E./M. Tech. in Electronics/Electrical/Instrumentation or similar with specialization in Communication Systems/Communication/VLSI/Microwave and Antenna or similar field with above 60% marks or 6.5 CGPA at both UG and PG levels</p> <p>Desirable Skills: GATE score is desirable for B.Tech. students. Coding Skills in MATLAB/Python/Verilog/Xilinx Vivado Previous research and work experience in the similar</p>	ETC	<p>Harish Kumar Sahoo <a href="mailto:harish_etc@vssut.ac.in">harish_etc@vssut.ac.in</a></p>

				domains will be given preference		
	H2B	Project Associate (PA)	Development of multi-channel remote patient monitoring with SoC RF transceiver.	<p><b>Essential Qualification:</b> M.E./M. Tech. in Electronics/Electrical/Instrumentation or similar with specialization in VLSI/Instrumentation/ Electronics/Electrical or similar with above 60% marks at both UG and PG levels OR B.E./B. Tech. in Electronics/Electrical/Instrumentation or similar with above 60% marks.</p> <p><b>Desirable Qualification:</b> GATE score is desirable. Previous experience in Digital and Analog IC design is desirable.</p>	<b>ETC</b>	Aditya Kumar Hota <a href="mailto:akhota_etc@vssut.ac.in">akhota_etc@vssut.ac.in</a>
	H3C	Project Associate (PA)	Self-powered wearable devices and drug delivery systems with biosensors for continuous healthcare monitoring.	<p><b>Essential Qualification:</b> Four-year bachelor's degree in chemical, biochemical, or biotechnology engineering or technology or medicine from a recognized university or equivalent.</p> <p><b>Or Master's degree in chemical, biochemical, engineering or technology or biotechnology medicine from a recognized university or equivalent.</b></p> <p><b>OR</b> <b>M.Sc. in life sciences</b></p>	<b>CHEM ENGG</b>	Lipika Parida <a href="mailto:lparida_chemical@vssut.ac.in">lparida_chemical@vssut.ac.in</a>

<b>Energy</b>						
	E4.6	Project Associate (PA)	RE Secure Control Protection Integrated Network and	<p><b>Essential qualification: BTech/ BE in Electrical/Electrical and Electronics/ Control Engineering or equivalent Engineering from a recognized university or equivalent with special interest in Power Systems, Microgrid Control, Operation, and Cybersecurity, Power Electronics and Drives, Active Power Filter, Renewable Energy &amp; EV, AI/ML-based Control Applications, Battery, Hydrogen Storage, Fuel Cells, Supercapacitors.</b></p> <p><b>Desirable qualification: MTech/ ME/ M.S. in Electrical / Control Engineering/ Energy Engineering/ Power System/Power Electronics/ Control and Instrumentation or equivalent, relevant to the area of research</b></p>	<b>EE</b>	Debidasi Mohanty <a href="mailto:ddmohanty_ee@vssut.ac.in">ddmohanty_ee@vssut.ac.in</a>
<b>Environment</b>						
	N1.2.2	Junior Research Fellow (JRF)/PA	Designing and fabrication of hybrid column for chromium removal at lab scale	<p><b>Essential Qualification: Post Graduate Degree in Chemistry or Graduate/ Post graduate Degree in chemical engineering, food technology, environmental engineering, biotechnology from a recognized university selected through a process described through any one of the following:</b></p>	<b>CHEM ENGG</b>	Krushna Prasad Shadangi <a href="mailto:kpshadangi_chemical@vssut.ac.in">kpshadangi_chemical@vssut.ac.in</a>

				<p>a) Scholars who are selected through National Eligibility Tests - CSIR-UGC NET, including lectureship (Assistant Professorship) and GATE.</p> <p>b) The selection process through National examinations conducted by Central Government Departments and their Agencies and Institutions, such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER, etc.</p>		
	N.8	Junior Research Fellow (JRF)/PA	Design and development of sustainable water lubricated bearings with modified structure, closed-loop water lubrication system for watercraft	<p>Essential qualification: Post Graduate Degree in Basic Science or Graduate/ Post graduate Degree in Professional course selected through a process described through any one of the following:</p> <p>a) Scholars who are selected through National Eligibility Tests - CSIR-UGC NET, including lectureship (Assistant Professorship) and GATE.</p> <p>b) The selection process through National examinations conducted by Central Government Departments and their Agencies and Institutions, such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER, etc.</p>	ME	<p>Padmanav Dash  <a href="mailto:pdash_me@vssut.ac.in">pdash_me@vssut.ac.in</a></p>

	N 2.2	Junior Research Fellow (JRF)/PA	Design and development of smart sensors and Li-Fi enabled sensor networks for detecting gas leakages	<p><b>Essential qualification: Post graduate Degree in Professional courses selected through a process described through any one of the following:</b></p> <p><b>a) Scholars who are selected through National Eligibility Tests - CSIR - UGE NET, including lectureship (Assistant Professorship) and GATE.</b></p> <p><b>b) The selection process through National examinations conducted by Central Government Departments and their Agencies and Institutions, such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER, etc.</b></p> <p><b>Preference shall be given to:</b>  <b>(I) Degree holders to M.Tech. in the discipline of Electronics and Communication Engineering/ Electronics and Instrumentation Engineering/ Instrumentation Engineering with specialization in Communication Engineering/ Communication System Engineering/ Instrumentation Engineering with at least 60% in aggregate or 6.5 CGPA on 10-point scale.</b>  <b>(II) GATE qualification.</b></p>	EEE	<p><b>Gyan Ranjan Biswal</b>  <a href="mailto:gyanbiswal@vssut.ac.in">gyanbiswal@vssut.ac.in</a></p>
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				<p><b>(III) Candidates with similar/related project experience</b></p> <p><b>Essential Qualification: B.Tech/B.E. in Civil Engineering with a minimum CGPA of 6.5 (60%)/1st class with valid GATE/NET score, and M.E./M.Tech in Structural Engineering/Construction Technology and Management/ Construction Management/ Construction Technology with a minimum CGPA of 6.5/Percentage 60% /1st class from a recognized Technological University.</b></p> <p><b>The candidate must be selected through a process described through any one of the following:</b></p> <p><b>a) Scholars who are selected through National Eligibility Tests - CSIR-UGC NET, including lectureship (Assistant Professorship) and GATE.</b></p> <p><b>b) The selection process through National examinations conducted by Central Government Departments and their Agencies and Institutions, such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER, etc.</b></p>		
	N 3.4.2	<p><b>Jounior Research Fellow (JRF)/PA</b></p>	<p>Development of Sustainable Self-Compacting Geopolymer Concrete through use of industrial and agricultural waste.</p>	<p><b>CIVIL ENGG</b></p>	<p>S. K. Panigrahi  <a href="mailto:skpanigrahi_ce@vssut.ac.in">skpanigrahi_ce@vssut.ac.in</a></p>	

ETC- 05  
CHEM ENGG – 02  
EEE – 01  
EE – 01  
ME – 01  
CIVIL E – 01

**Total – 11 (4 JRF/PA + 7 Project Associate)**