



VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA, ODISHA, INDIA

ବୀର ସୁରେନ୍ଦ୍ର ସାଏ ବିଶ୍ୱବିଦ୍ୟାଳୟ

(A UGC Recognized State Government University by an Act of Assembly, Estd. -1956)

P.O. Engineering College, Burla, Dist: Sambalpur, Odisha, (India) -768 018

www.vssut.ac.in. e-mail: vc@vssut.ac.in

Notification No. VSSUT/ PGS&R/ 585 / 2026

Dated: 10/04/2026

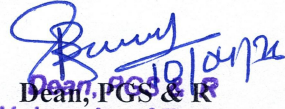
PROVISIONAL

AWARD NOTIFICATION
THE DEGREE OF DOCTOR OF PHILOSOPHY IN ENGINEERING

Mr. Mukesh Kumar Sukla, Registration No: 1810070009 is declared to have qualified for the award of the Ph.D. Degree in Engineering (Department of Electronics and Telecommunication Engineering) with effect from 16.03.2026 (Monday). The title of the thesis is “LOW-POWER AREA-EFFICIENT AND ACCURACY TRADE-OFF ARITHMETIC CIRCUITS FOR IMAGE PROCESSING APPLICATIONS”.

YEAR OF EXAMINATION - 2026
REGISTRATION NO. - 1810070009

By order of Hon'ble Vice-Chancellor

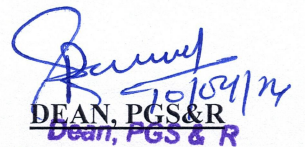

Dean, PGS & R

VSS University of Technology
Burla, Odisha, India-768018
Date: 10/04/2026

Memo.No.VSSUT/PGS&R/ 586 /2026

Copy forwarded to:

1. Mr. Mukesh Kumar Sukla, S/o- Mr. Kasinath Sukla, At: Flat No-201, Rajarani Sweet Homes, Alekha Nagar, Khodasingi, Berhampur, Ganjam, Odisha, -760010, India.
2. Dr. Kabiraj Sethi, Retired Associate Professor, Department of Electronics and Telecommunication Engineering, VSSUT, Burla - Supervisor
3. Prof. (DR.) Mridula Gupta, Professor, Department of Electronics Science, University of Delhi, New Delhi, India – External Examiner of Ph.D. Open Viva-voce (Indian Examiner).
4. Prof. Lan Fu, The Australian National University, Canberra, Australia –External Examiner (Foreign).
5. The Chairman and all members of DRC of Mr Mukesh Kumar Sukla, Department of Electronics &Telecommunication Engineering, VSSUT, Burla for information.
6. The Director of Printing & Publication, Govt. Press, Madhupatna, Cuttack, Odisha, Pin-753010 for publication in the next issue of Odisha Gazette. **Free of Cost.**
7. The Principal Secretary to Chancellor, Raj Bhavan, Bhubaneswar, Pin-751008, for kind information.
8. The Principal Secretary to Government of Odisha, Skill Development & Technical Education Department, 2nd floor, Kharvel Bhavan, Bhubaneswar, Pin-751001, for kind information.
9. The University Grants Commission (UGC), Bahadur Shah Zafar Marg, New Delhi - 110002.
10. The Secretary/ Head Librarian, University News, Association of Indian Universities, AIU House, 16,Comrade Indrajit Gupta Marg, Opposite National Bal Bhavan, Near ITO, New Delhi -110002, with a request to publish in University news. **Free of Cost.**
11. The Editor, The Samaj/ The Sambad/ The Dharitri/The Samaya/ The Prameya/ The New Indian Express/ The Times of India ,with a request to publish the same in their News Daily, 110002. **Free of Cost.**
12. The Controller of Examinations/All Deans/All HODS/COF, VSSUT, BURLA for information.
13. Dean, Faculty & Planning, VSSUT, Burla for kind information with a request to hoist this notification in the University Website for information of all concerned.
14. PA to VC for kind information of Vice-Chancellor.
15. Guard File (PGSR)/Personal file of Ph.D. Scholar.


DEAN, PGS&R
Dean, PGS & R

VSS University of Technology
Burla, Odisha, India-768018