

VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY,BURLA
RESULT OF 3RD SEMESTER (M.TECH) EXAMINATION NOVEMBER 2016 (GRADE LIST)

BRANCH :ELECTRONICS & TCE (VLSI SIGNAL PROCESSING)

3RD SEMESTER		SUBJECTS->		DIE	CVV	SOD	SGPA	CGPA
SL	REG NO.	NAME	CREDIT->	010	03	02		
1	15040261	SHARMISTHA NAYAK		E	A	E	8.80	8.51
2	15040262	PRASANT SWAIN		E	A	E	8.80	8.00
3	15040264	SANTOSH KUMAR BALABANTARAY		A	B	B	7.67	6.86
4	15040266	JYOTIPRAKASH SWAIN		A	A	A	8.00	6.65
5	15040267	SNIGDHA SUBHADARSHINEE TRIPATHY		E	B	A	8.47	8.38
6	15040268	AMAR KISHOR		E	A	E	8.80	7.69
7	15040270	JITENDRA KUMAR SWAIN		E	E	E	9.00	8.15
8	15040271	SAGAR PUHAN		E	B	A	8.47	7.68
9	15040272	SATABDI ACHARYA		O	E	E	9.67	8.97
10	15040273	PRIYADARSHEE PANI		E	E	E	9.00	8.32
11	15040274	ANURADHA VERMA		E	E	A	8.87	8.69
12	15040275	LOPA NAYAK		E	E	A	8.87	8.46
13	15040276	JHOSTNA RANI BEHERA		E	E	E	9.00	7.76

WH* - Withheld due to Other reason

DIE-->DISSERTATION INTERIM EVALUATION

CVV-->COMPREHENSIVE VIVA -VOCE

SOD-->SEMINAR ON DISSERTATION

(CONTROLLER OF EXAMINATIONS)