VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY,BURLA
RESULT OF 1ST SEMESTER (M.TECH) EXAMINATION NOVEMBER 2016 (GRADE LIST)

BRANCH :ELECTRONICS & TCE (COMMUNICATION SYSTEM ENGG.)

1ST SEMESTER		SUBJECTS->	ADELMG	AVLD	ADCT	ASP	ECC&C	TSELAB-I	DSLAB	SEMN-I	CVV-I	SGPA	CGPA
SL	REG N0.	NAME CREDIT->	04	04	04	04	04	02	02	02	02		
1	1605070001	AMLAN PATEL	B+	F	B+	B+	B+	A	A+	A	O	*.**	*.**
2	1605070002	ALLEN PATTNAIK	B+	C	B+	A	B+	A+	A	A	O	7.36	7.36
3	1605070003	AMIT KUMAR BEHERA	B	F	F	B+	C	A	B+	B+	A+	*.**	*.**
4	1605070004	SUBHENDU SAHU	C	P	F	B	F	A	B+	B+	A+	*.**	*.**
5	1605070005	BIBHUDATTA MISHRA	F	F	F	B	AB	B+	B+	B+	A	*.**	*.**
6	1605070006	IVARANI ROUTRAY	A	B	B+	A	B+	A+	A	A	A+	7.57	7.57
7	1605070007	SOUMYASHREE PRADHAN	B	F	C	B+	B	A	A	B+	B+	*.**	*.**
8	1605070008	MADHUSMITA PATI	F	F	C	C	F	A	B+	A	A	*.**	*.**
9	1605070009	SRADHANJALI PRADHAN	AB	AB	AB	B+	AB	A	B+	A	B+	*.**	*.**
10	1605070010	PALLAVI PADHAN	B	F	F	C	F	B+	A	B+	A	*.**	*.**
11	1605070011	SWATI KUMARI	A	C	B+	B	B	A+	A	A+	A	7.00	7.00
12	1605070012	SMEETA HOTA	B+	P	B	B	C	A+	B+	A+	A+	6.43	6.43
13	1605070013	SUSHANTA KUMAR SETHI	B+	F	C	C	P	A	A	B+	A	*.**	*.**

WH* - Withheld due to Other reason

ADELMG-->ADVANCED ELECTROMAGNETICS

AVLD-->ANALOG VLSI DESIGN

ADCT-->ADVANCED COMMUNICATION TECHNIQUES

ASP-->ADVANCED SIGNAL PROCESSING

ECC&C-->ERROR CONTROL CODING & CRYPTOGRAPHY

TSELAB-I-->TELECOMMUNICATION SYSTEM ENGINEERING LAB.-I

DSLAB-->DESIGN AND SIMULATION LABORATORY

SEMN-I-->SEMINAR-I

CVV-I-->COMPREHENSIVE VIVA VOCE-I

(CONTROLLER OF EXAMINATIONS)