

**VEER SURENDRA SAI UNIVERSITY OF TECHNOLOGY, BURLA**  
**RESULT LIST OF 1ST SEMESTER (PH.D) EXAMINATION (BACKLOG-GRADELIST)**

**BRANCH : ELECTRONICS & TC ENGINEERING**

**SEMESTER : 1st**

| 1st Semester |          | SUBJECT->         |           | BE | CHEM | COMPROG | ESCE | MATH-I | RM |
|--------------|----------|-------------------|-----------|----|------|---------|------|--------|----|
| SL           | REGD.NO  | NAME              | CREDIT -> | 04 | 04   | 04      | 04   | 04     | 04 |
| 1            | 16060011 | ASIT KUMAR MISHRA |           | *  | *    | *       | *    | *      | D  |

**(CONTROLLER OF EXAMINATIONS)**